

11002 U.S. PT
10/033122
10/22/01

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPLICATION		FILING DATE		CLASS	SUBCLASS	GAU.	EXAMINER
APPL NUM	10033122	FILING DATE	10/22/2001	257	328	2811	<i>Warren</i>
**APPLICANTS: Tihanyl Jenoe;							
**CONTINUING DATA VERIFIED:							
** FOREIGN APPLICATIONS VERIFIED: GERMANY 100 52 004.9-33 10/20/2000							
PG-PUB		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO			
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no		WMP-SME-519			
Verified and Acknowledged Examiners's initials							
TITLE : Vertical field-effect transistor with compensation zones and terminals at one side of a semiconductor body							

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	FIG. Drwg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
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